



XA-9519A  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hidenori SATO et al.

Appln. No.: 10/642,658

Group Art Unit: 2812

Filed: August 19, 2003

For: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action dated January 28,  
2004, please amend the above-identified patent application  
as indicated below.

Amendments to the claims are reflected in the listing  
of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.